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PATENT & TRADEMARK OFFICE
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Docket No.: 050195-0392 **PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshio SHIMOIDA, et al.

RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE

Application No.: 10/678,208

Customer No.: 20277

Filed: October 06, 2003

Confirmation No.: 2117

Group Art Unit: 2818

Examiner: Ngo, Ngan V

Title: STACKED SEMICONDUCTOR MODULE AND ASSEMBLING METHOD OF THE SAME

AMENDMENT UNDER 37 C.F.R. § 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following Amendment and Remarks are submitted in response to the Office Action dated June 24, 2005. Please amend the above-identified application as follows.